

10BASE-T LOW PASS FILTER, P/N:HST-68026

Features:

- IEEE 802.3 and IEEE 802.5 compatible
- Designed to meet AMD 79C98.AT& T T7220& NS DP83922 chip set requirements.
- Low insertion loss characteristics
- Cost effective designs.

Specifications: at 25 degree C.

- Insertion loss(1-10MHz):transmitter 1.0 dB max. Receiver 1.0 dB max.
- Return loss(5MHz):15dB min.
- Crosstalk(1-10MHz): 30 dB min.
- Group delay change: 2.0 ns max.
- Hi-pot test:1500Vrms.
- Type:transmitter 7 pole,receiver 5 pole.
- Cut-Off frequency:17MHz± 0.7MHz.
- Attenuation:
 - @20 MHz transmitter:7 dB Min; receiver:6dB Min.
 - @25 MHz transmitter:19 dB Min; receiver:14dB Min.
 - @35 MHz transmitter:32 dB Min; receiver:20dB Min.
 - @40 MHz transmitter:35 dB Min; receiver:31dB Min.

Common choke specification:

- Turn Ratio:1:1:1:1
- Primary inductance (LP):18-36uH,
- Leakage inductance(LL):0.3uH.
- Primary capacitance(C_{ww}):25pF.

Dimension:mm(inch)+/-0.25(0.01)

Schematic:

